[CEG3420 Lab 3 Report]

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| --- | --- | --- |
| [Your Name] |  | [SID] |

# Task 1

**Describe the outline of how to implement the function “eval\_micro\_sequencer()” here:**

**Paste your experimental results of** **“run 10” here by running “toupper.cod”.**

**(You need paste the information of instructions and registers)**

# Task 2

**Describe the outline of how to implement the function eval\_bus\_drivers() here:**

**Describe the outline of how to implement function “drive\_bus()” here:**

**Paste your experimental results of “run 10” here by running “count10.cod”.**

**(You need paste the information of instructions and registers)**

# Task 3

**Describe how you implement the “LD.MAR” in the function “latch\_datapath\_value()”:**

**Describe how you implement the “LD.IR” in the function “latch\_datapath\_value()”:**

**Describe how you implement the “LD.CC” in the function “latch\_datapath\_value()”:**

**Describe how you implement the “LD.PC” in the function “latch\_datapath\_value()”:**

**Describe the influence of the different SID in this task and how you resolve it.**

**Paste your experimental results of “run 10” here by running “toupper.cod” using corresponding ucode3 file (** **ucode3-even for students with even ending SID, ucode3-odd for students with odd ending SID) .**

**(You need paste the information of instructions and registers)**